

	L #	Hit	S arch T xt	DB
1	L1	3060	(257/691,723,724,777,924).cccls.	USP AT; US-P GPU B
2	L2	1885	(361/760,763,782,783).cccls.	USP AT; US-P GPU B
3	L3	4740	1 2	USP AT; US-P GPU B
4	L4	292	3 and @pd>=20030407	USP AT; US-P GPU B
5	L5	2	4 and (charg\$3 with pump\$3)	USP AT; US-P GPU B
6	L6	87	4 and (capacitor\$1 condenser\$1 inductor\$1 (passive adj (component\$1 device\$1 element\$1)))	USP AT; US-P GPU B